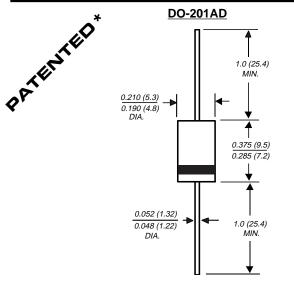
1N5624GP THRU 1N5627GP

GLASS PASSIVATED JUNCTION RECTIFIER

Reverse Voltage - 200 to 800 Volts Forward Current - 3.0 Amperes



Dimensions in inches and (millimeters)

* Glass-plastic encapsulation technique is covered by Patent No. 3,996,602 and brazed-lead assembly by Patent No. 3,930,306



FEATURES

- Plastic package has Underwriters Laboratory
 Flammability Classification 94V-0
- High temperature metallurgically bonded construction
- Glass passivated cavity-free junction
- Capable of meeting environmental standards of MIL-S-19500
- ◆ 3.0 Ampere operation at TA=70°C with no thermal runaway
- Typical IR less than 0.1µA
- High temperature soldering guaranteed: 350°C/10 seconds, 0.375" (9.5mm) lead length 5 lbs. (2.3kg) tension

MECHANICAL DATA

Case: JEDEC DO-201AD molded plastic over glass body Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026 Polarity: Color band denotes cathode end Mounting Position: Any Weight: 0.04 ounce, 1.12 grams

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

		SYMBOLS	1N5624GP	1N5625GP	1N5626GP	1N5627GP	UNITS
* Maximum repetitive peak reverse voltage		Vrrm	200	400	600	800	Volts
* Maximum DC blocking voltage		VDC	200	400	600	800	Volts
 Maximum average forward rectified current 0.375" (9.5mm) lead lengths at T_A=70°C 		l(AV)	3.0			Amps	
 * Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) 	ave superimposed		125.0			Amps	
* Maximum instantaneous forward voltage at 3.0A	Ta=25°C Ta=70°C	VF	1.0 0.95			Volts	
Maximum reverse current at rated DC blocking voltage	T _A =25°C T _A =150°C	IR	30	<u>5.</u> 00.0	.0 200.0		μA
Maximum full load reverse current, full cycle average, 0.375" (9.5mm) lead length at TA=70°C		IR(AV)		20	200.0		μΑ
ypical reverse recovery time (NOTE 1)		trr	3.0			μs	
Typical junction capacitance (NOTE 2)		CJ	40.0			pF	
Typical thermal resistance (NOTE 3)		Roja	20.0			°C/W	
Operating junction and storage temperature range		TJ, TSTG		-65 to +175			°C

NOTES:

(2) Measured at 1.0 MHz and applied reverse voltage of 4.0 V_DC

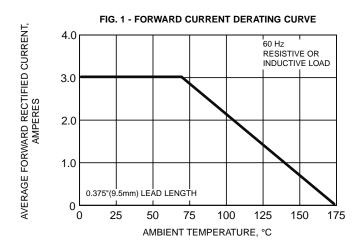
(3) Thermal resistance from junction to ambient at 0.375" (9.5mm) lead length, P.C.B. mounted

*JEDEC Values



⁽¹⁾ Reverse recovery test conditions: IF=0.5A, IR=1.0A, Irr=0.25A

RATINGS AND CHARACTERISTIC CURVES 1N5624GP THRU 1N5627GP



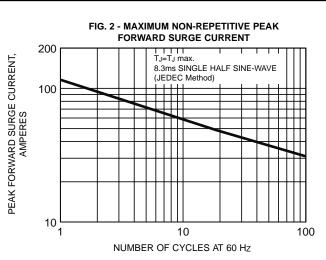
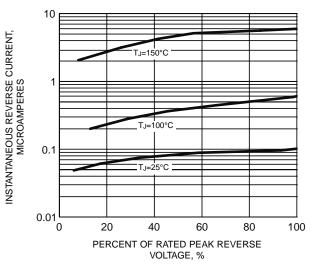
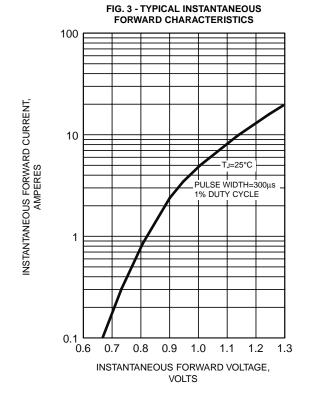
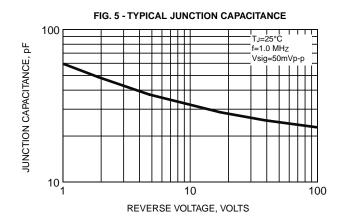


FIG. 4 - TYPICAL REVERSE CHARACTERISTICS







GENERAL SEMICONDUCTOR[®]